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# Apple



# Assembly

Line

Volume 5 -- Issue 3

December, 1984

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#### New Source for 65802's

I talked to Constantine Geromnimon at Alliance Computers this morning. His company has ordered hundreds of 65802's, and offers them to you at \$49.95 each. They expect their next shipment to come in around the middle of January, so now is the time to order. Call them at (718) 672-0684, or write to P. O. Box 408, Corona, NY 11368.

#### Tom Weishaar Writes Again!

If you are among the throng who mourn the passing of Softalk, and particularly of the many informative columns such as DOStalk by Tom Weishaar, you will be as glad as I am that Tom has started publishing his own monthly newsletter.

Called "Open-Apple", you can subscribe for \$24. In an unprecedented move toward international goodwill and the wholesome exchange of information, Tom has set the price the same for everyone, everywhere. We promptly sent him a check. If you love your Apple, do likewise. Send to Open-Apple, 10026 Roe, Overland Park, Kansas 66207. If you are cautious, send no money; Tom will bill you with the first issue, and you can cancel if you lose interest.

18-Digit Arithmetic, Part 8..................Bob Sander-Cederlof

Someone pointed out last week that this series is getting a little long. Well, we are nearing the end. What we are doing is probably unprecedented in the industry: listing the source code and explaining it for a large commercially valuable software product. It takes time and space to break precedents.

This month's installment completes the normal set of math functions, with sine, cosine, and arc tangent. We even slipped in a simple form of the tangent function. Still to come are the formatted INPUT and PRINT routines.

#### Some Elementary Info:

Trigonometry is a frightening word. (If it doesn't scare you, skip ahead several paragraphs.) The "-ometry" refers to measurement, but what is a "trigon". Believe it or not, "trigon" is another name for a triangle. Trigon means three sides, and figures with three sides just happen to also have three angles. "Trig" (a nice nickname) is a branch of mathematics dealing with triangles, without which we could not fly to the moon, draw a map, or build bridges. Strangely enough, much of electronics also uses trig funtions ... are electrons triangular?

When I took trig in high school, long before the day of personal calculators, we used trig tables. (These were not articles of furniture made in the local woodshop, but rather long lists of strange numbers printed and bound into books.) The tables contained values for various ratios of the sides of a triangle having one 90-degree angle. Now we use calculators or computers, but obviously the trig tables would not fit in them. Instead, approximation formulas are used.

In high school, we talked about six different ratios: sine, cosine, tangent, cotangent, secant, and cosecant. When it is all boiled down, we really only need the sine; all the rest are derivable from those. The sine function gives a a number for any angle. We frequently need to be able to go from a trig value back to an angle, and the most useful function for that is called the inverse tangent, or arctangent.

Even though I have been talking about triangles, trig functions are even more related to circles. We compute functions of the angle between any two radii, like the hands on an old-fashioned, pre-digital wrist watch. When we start talking about circles, we get into radians vs. degrees.

Just as scientists like logarithms to the base e (rather than 10), they also like trig functions based on angles expressed in radians, rather than degrees. Degrees were invented back in Babylon, I understand, and are nice and clean: 360 make a complete circle. Radians are not clean: 360 degrees is two-times-pi radians. Nevertheless, many physical and electronic formulas simplify when angles are expressed in radians. Consequently, calculators and computer languages usually expect your angles to be expressed in radians. Some

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allow both options. Applesoft expects radians, and so do my DP18 programs.

We commonly think of an angle as being somewhere between 0 and 360 degrees, or the equivalent range in radians. However, angles can actually be any number, from -infinity to +infinity. The numbers beyond one complete circle are valid, but they don't buy much. If you stand in one place and spin around 1445 degrees (4\*360+5) you will end up pointing the same direction as if you merely swiveled 5 degrees. Therefore the first step in a sine function calculation involves subtracting out all the multiples of a full circle from the angle.

The arctangent function could return an infinite number of answers, but that is impractical. We will return only the principal value, which is the one closest to 0. All others are that value plus or minus any number of full circles. In DP18 the ATN function may have one or two arguments. If you only have one argument, the result will be an angle between -pi/2 and +pi/2. If you specify two arguments, a value between -pi and +pi will be returned.

#### The Nitty-Gritty:

Enough of this preliminary stuff, let's get into the code. In the listing which follows, you will find entries for four functions: SIN, COS, TAN, and ATN.

Perhaps the easiest is the TAN function, at lines 2530-2630. Since tan=sin/cos, that is all this code does. We lose a little speed and possibly some precision with this simplistic solution, but the TAN function is relatively rarely called.

Next in difficulty is the COS function, lines 1630-1710. Since  $\cos(-x)=\cos(x)$ , we start by making the sign positive (lines 1690-1700. Since  $\cos(x)=\sin(x+pi/2)$ , we add pi/2 and fall into the SIN function. Simple, but effective.

The SIN function gets more interesting. For very very small angles, within the precision of 20 digits,  $\sin(x)=x$ . Lines 1780-1810 check for exponents below -10; all angles smaller than 10^-10 are small enough that  $\sin(x)=x$ .

Next we take advantage of the fact that sin(-x)=-sin(x), at lines 1820-1860. We remember the sign by shoving it on the stack, and force the sign of x positive.

Lines 1870-1950 get the principal angle. I divide x by twopi, and throw away the integral part. The fractional part that remains is a fraction of a full circle, a value between 0 and .999999...9 (not radians, and not degrees either). Note that if x was extremely large there will be no fractional part, and the remainder will be zero. Some SIN function calculators give an error message when this happens, but I chose to let it ride.

Lines 1960-2000 multiply the circle-fraction by four. This gives a number between 0 and 3.99999...9, which I will refer to later as the "circle fraction times four", or c-f-t-f. The

integer part is effectively a quadrant number, and the fractional part a fraction within the quadrant:

Lines 2010-2030 determine if the angle is in the first (0) quadrant. If so, no folding need be done.

Lines 2040-2070 determine if the angle is in the second (1) quadrant. If so, we skip ahead to apply the fact that  $\sin(pi/2 + x) = \sin(pi/2 - x)$ .

Lines 2080-2160 are executed if the angle is in the 3rd or 4th quadrants (integral part is 2 or 3). Here I apply the fact that  $\sin(pi+x)=-\sin(x)$ . I pull the saved sign off the stack, complement it, and shove it back on (lines 2090-2110). Then I subtract 2 from the c-f-t-f, yielding a number between 0 and 1.99999...9. We have folded the third and fourth quadrants over the first and second quadrants. Next lines 2170-2190 determine if the result was in the first quadrant or not.

Lines 2200-2240 fold a second quadrant number into the first quadrant, by applying the fact that  $\sin(pi/2+x) = \sin(pi/2-x)$ . Subtacting the c-f-t-f from 2 flips us into the first quadrant.

Lines 2260-2270 pull the sign off the stack and make it the sign of the angle. Remember that now the angle is a fraction (between 0 and .99999...9) of a quadrant. After all these folding operations, the angle might again be very very small, so lines 2280-2300 check for that possibility. If so,  $\sin(x)=x$ , but that is only true when x is in radians. Lines 2490-2520 convert the quadrant-fraction to radians by multiplying by pi/2, and exits.

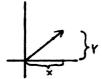
Lines 2310-2470 handle larger angles by computing x\*P/Q, where P and Q are polynomials in  $x^2$ . The constants for P and Q are given in lines 1420-1550, and come from the Hart book. [ I should mention here that I wrote those constants with pretty periods separating groups of five digits. This will not assemble in some older versions of the S-C Macro Assembler. If you get a syntax error, just leave out the periods.]

#### Turning the Tables:

ATN is hardest to compute. First we have to deal with the two variants of calls, having one or two arguments. While all the previous function programs were called with the argument already in DAC, DP.ATN is called immediately after parsing the ATN-token. Lines 2960-3070 parse and process the following parentheses and whatever is between them.

Lines 2960-2970 require an opening parenthesis. Line 3070 requires the closing parenthesis. In between we expect one expression, or two expressions separated by a comma. If there is only one, we fake a second one (=1.0).

What are the two arguments? Looking at a cartesian system, with the vector shown below, the arguments are (Y,X). If you call with one argument, it is (Y/X).



By using two separate arguments, rather than just the ratio, we can tell which of the four quadrants the vector was in. DP.ATAN will return a value between -pi and +pi, depending on the two signs. If you specify only the ratio, DP.ATAN will return a value between 0 and +pi depending on the sign.

Lines 3120-3160 save the two signs in bits 6 and 7 of UV.SIGN. Way at the end, lines 4100 and following, UV.SIGN determines the final value. If the sign of the denominator (X-vector) was negative, the composite vector is in the 2nd or 3rd quadrant: computing pi - angle gives a result between pi/2 and pi.

If the numerator (Y-vector) was negative, the composite vector is in the 3rd or 4th quadrant. Flipping the sign gives a result between 0 and -pi.

Lines 3180-3220 check for special cases of Y=0 or X=0. If the first argument (Y-vector) is zero, the angle is 0 or pi depending on the sign of the second argument. If the second argument (X-vector) is zero, the angle is either +pi/2 or -pi/2, depending on the sign of the first argument. What if both arguments are zero? That should produce an error message, but I am overlooking it: I will return an angle of 0 in this case.

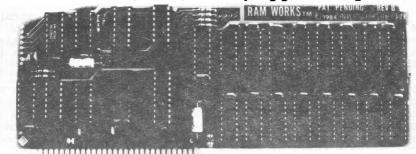
If neither argument is zero, some special checks are made to see if the value of the ratio is very small or very large. I check before actually dividing, so the divide routine won't kick out on an overflow error. If the ratio would be greater than  $10^2$ , I return a value of pi/2. This is accurate within the precision of DP18. On the other hand, if the ratio is smaller than  $10^2$ -63 I return 0. If neither extreme is true, I go ahead an divide to get the actual ratio. Then I check for an extremely small ratio, in which case atan(x)=x.

If we find our way down to line 3390, the ratio is between  $10^--10$  and  $10^-20$ . That is still too large a range for comfort, so we apply the fact that  $\operatorname{atan}(1/x) = \operatorname{atan}(\operatorname{pi}/2 - x)$ . If the ratio of Y/X is greater than 1.0, then we take the reciprocal and remember that we did so. This in effect folds the range at  $\operatorname{pi}/4$ . The resulting argument range is between  $10^--10$  and 1. The variable N holds either 0 or 2 as a flag: 0 if we were already under 1, 2 if we formed the reciprocal.

The shape of the curve of the arctangent function between 0 and 1 (an angle between 0 and pi/4) is deceptive. It looks nice

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and easy, but a polynomial over that range with 20 digits of precision is much too long. We can easily reduce the range still further by applying another identity. If the reduced argument is now already below  $\tan(pi/12)$ , fine. If not, calculating (x\*sqr(3)-1) / (sqr(3)+x) will bring it into that range. If we have to apply that formula, N will be incremented (making it 1 or 3).

The curve between 0 and tan(pi/12) looks almost like a straight line to the naked eye, but it really is far from straight. It takes a ratio of the form P/xQ where P and Q are polynomials in  $x^2$ . The coefficients are given in lines 2650-2770, again from Hart. The ratio is computed in lines 3800-3960.

Lines 3970-4080 start the unfolding process. The variable N is either 0, 1, 2, or 3 by this time. If N is 0, no folding was done. If N is 1, only folding above pi/12 was done. If N is 2, only folding above pi/4 was done. If N is 3, both folds were done. These lines convert the angle back to the correct value, using a table of addends and an optional sign flip:

N	unfolding formula
0	none
2	pi/6 + x pi/2 - x
3	pi/2 - (pi/6 + x) = pi/3 - x

That's it! We already discussed the code beyond line 4100, which figures out which quadrant the angle is in.

Any questions?

```
1000 *SAVE S.DP18 TRIG
                            .EQ $B1
.EQ $B7
.EQ $DEBB
.EQ $DEB8
B1-
B7-
                            1040 AS.CHKCLS
1050 AS.CHKOPN
DEBB-
DEB8-
                            1050
                                                        EQ $FFFF
                           1070 POLY.1
1080 POLY.N
1090 DADD
FFFF-
FFFF-
FFFF-
FFFF-
                            1100 DSUB
                            1110 DMULT
                           1120 DDIV
1130 DP.INT
1140 DP.EXP
FFFF-
FFFF-
                           1150 DP.TRUE .E.
1160 DP.FALSE .E.
1170 MOVE.DAC.ARG
1180 MOVE.YA.ARG.1
                                                        EQ SFFFF
FFFF-
FFFF-
                                                               FFFF-
                           1190 MOVE TA DAC 1
1200 SWAP ARG DAC
1210 MOVE DAC TEMP1
1220 MOVE DAC TEMP2
FFFF-
FFFF-
FFFF-
                           1230 MOVE.DAC.TEMP3
1240 MOVE.TEMP1.DAC
FFFF-
FFFF-
                           1250 MOVE. TEMP1. ARG .EQ
1260 MOVE. TEMP2. ARG .EQ
FFFF-
FFFF-
                           1270 MOVE.TEMP3.ARG .EQ
1280 PUSH.DAC.STACK .EQ
FFFF-
                           1290 POP.STACK.ARG
```

```
1300 #1
1310 DA
1320 DA
1330 DA
1340 #1
1350 AB
1360 AB
1370 AB
1380 #1
0800-
0801-
080B-
                                                           .BS
.BS
                                                                  10
                                 DAC. EXPONENT
                                 DAC.HI
DAC.SIGN
                                                           .BS
.BS
.BS
080C-
                                  ARG. EXPONENT
                                  ARG.HI
ARG.SIGN
080D-
0817-
                                                                 10
0818-
0819-
                                                    .BS
                                                           1
                          1400
                                                    .BS
                                  UV. SIGN
                          1410
                          1420 P.SIN
1430 P.SIN.N
                                                    .EQ 6
081A-
06-A-
081D-
0820-
0828-
0828-
0828-
0833-
0838-
0838-
0838-
0844-
0846-
                                                                   P6*X^6 + P5*X^5 + ... + P1*X + P0
                    31
84
41
         2648E09106322400367246765118
               5364885163132437547847171636
                          1440
                                                    .HS 3C.50312.63884.64664.12845
                                                                                                            P6
                    81
39
73
                          1450
                                                    .HS BE.82818.08039.29577.39110
                                                                                                            P5
                    91
90
35
                         1460
                                                    .HS 40.62919.63490.93113.55230
                                                                                                            P4
                    64
36
85
                         1470
                                                    .HS C2.25642.44036.60338.57070
                                                                                                            P3
                    89
53
87
                                                    .HS 43.53892.64053.57788.76289
                         1480
                                                                                                            P2
                    32
70
23
0857-
085A-
085C-
085F-
0862-
0865-
                         1490
                                                    .HS C4.49326.67470.47152.36677
                                                                                                            P1
                    58
                    54
                                                    .HS 45.12596.16380.91365.41816
                         1500
                                                                                                            PO
                         1510
1520
1530
0867-
                                 Q.SIN.N
                                                    .EQ *
02-
0867-
086A-
086D-
0870-
                                                                   X^2 + Q1#X + Q0
                   74
16
41
          43334677
               1531950
31950
6727
                         1540
                                                    .HS 43.15743.43316.33194.13935
                                                                                                            Q1
0872-
0875-
0878-
0878-
                    18
36
71
                                                    .HS 44.80189.66936.87727.15787
                         1550
1560
                                                                                                           00
087D-
0880-
0883-
0886-
0888-
088E-
               10
          00
              000000002359538216755497
                    00
          00
41
00
00
                                                    .HS 41.10000.00000.00000.00000
                         1570 CON.ONE
                   00
                   00
0891-
0893-
0896-
0896-
0896-
0896-
0844-
         0118777199119830445935
                                                    .HS 41.20000.00000.00000.00000
                         1580 CON . TWO
                    87
86
                         1590 CON.2PI
                                                    .HS 41.62831.85307.17958.64769
                   70
26
96
                         1600 CON.PI.2
                                                    .HS 41.15707.96326.79489.66192
                   41
53
93
08A9-
08B2-
08B4-
                         1610 CON . PI
                                                    .HS 41.31415.92653.58979.32385
                   91
30
53
08BA-
08BD-
                         1620 CON.1..2PI .HS 40.15915.49430.91895.33577
                                                                                                           1/2PI
```

```
1630 COS (DAC)
1640 COS (DAC)
1650 COS LDA CON PI . 2
 08BF- A9 9E
08C1- A0 08
                                                     LDA #CUN.FI.2

LDY /CON.FI.2

JSR MOVE.YA.ARG.1 COS(X) = SIN(X+PI/2)

GET ABS(DAC) TO FORCE

COS(_Y)=COS(X)
                                                                                        PI/2
 08C1- A0
08C3- 20
08C6- A9
08C8- 8D
                              1670
1680
                  FF
                        FF
            Ã9
8D
20
                  00
                              1690
                        08
                  0B
                              1700
                                                     STA DAC.SIGN
JSR DADD
                  FF
                        FF
                              1710
                              1720
1730
1740
1750
                                                     SIN (DAC)
                                                     #3371
                                       .
                              1760
1770
1780
1790
1800
1810
                                       DP.SIN
                                                     X VERY SMALL...-
LDA DAC.EXPONENT
CMP #$40-10
BCS .1
 08CE- AD 00 08
08D1- C9 36
08D3- BO 01
08D5- 60
                                                                                        NOT VERY SMALL
VERY SMALL, SIN(X)=X
                                                     RTS
                                               ADJUST FOR SIGN OF X-
LDA DAC.SIGN
                              1820
                                                                                        SIN(-X) = SIN(X)
...SO SAVE SIGN OF X
...AND MAKE X POSITIVE
08D6- AD
08D9- 48
08DA- A9
08DC- 8D
                              1830
1840
1850
                  0B 08
                                        .1
                                                     PHA
                                                           #0
                  იი
                                                     LDA
                              1860
1870
1880
1890
                                              STA DAC.SIGN
                       08
                  0B
                                                     LDA #CON.1..2PI
LDY /CON.1..2PI
08DF- A9
08E1- A0
                  B4
08
                              1900
1910
1920
1930
08E3-
08E6-
            20
20
                 FF
                       FF
                                                     JSR MOVE.YA.ARG.1
                                            JSR DMULT
--GET FRACTIONAL PART
JSR MOVE.DAC.ARG
                 FF FF
 08E9- 20
                 FF
                       FF
08EC- 20
08EF- 20
                 FF FF
                              1940
                                                     JSR DP.INT
                             1950
1960
1970
1980
1990
2000
2010
                 FF
                       FF
                                                     JSR DSUB
                                                     D QUADRANTS INTO
JSR MOVE.DAC.ARG
                                                                                     ONE:
            20
20
20
20
                                                                                       MULTIPLY BY FOUR
 08F2-
08F5-
08F8-
                 FF
FF
                       FF
                                                     JSR DADD
                                                                                        BY DOUBLING TWICE
                                                     JSR MOVE.DAC.ARG
                                                     JSR DADD
 08FB-
                 FF
                       FF
08
                                                                                       0 <= DAC < 17
 08FE- AD
                  00
                                                     LDA DAC.EXPONENT
                             0901- C9
0903- 90
                  41
                 29
                                                                                        ...YES, IT IS IN 1ST QUADRANT
                                                     BCC
                                              -2ND, 3RD, OR 4TH-
LDA DAC.HI
CMP #$20
0905- AD
0908- C9
090A- 90
                 01
                       80
                 20
                                             CMP #$20 IS DAC < 2.0?
BCC .3
-FOLD 3RD-4TH OVER 1ST-2ND----
090C- 68
090D- 49
090F- 48
                                                    PLĂ
                                                                                        ...NO, FLIP SIGN FOR 3RD OR 4TH QUADRANTS
                 80
                                                     EOR #$80
                                                    PHA
           A9
A0
                  88
08
                                                    LDA
                                                                                       FOLD 3RD & 4TH OVER 1ST & 2ND
0910-
0912-
                                                            #CON . TWO
                                                     LDY
0914- 20
0917- 20
091A- 20
091D- AD
                                                     JSR MOVE.YA.ARG.1
                 FF FF
                FF FF
FF FF
OO OB
41
OA
                                                    JSR SWAP.ARG.DAC
JSR DSUB
LDA DAC.EXPONENT
                                                    CMP #$41
BCC .4
0920-
0922-
           Ç9
                                              BCC .4
-FOLD 2ND OVER 1ST
                                                                                       ... ALREADY IN 1ST
0924- A9 88
0926- A0 08
0928- 20 FF
092B- 20 FF
                                                    LDA #CON.TWO
LDY /CON.TWO
                            •3
                                                                                       LET X=2-X
                                                    LDY /CON.TWO
JSR MOVE.YA.ARG.1
                       FF
                                                     JSR DSUB
                                             -ANGLE NOW IN 1ST QUADRANT-
PLA PUT F
STA DAC.SIGN
LDA DAC.EXPONENT CHECK
CMP #$40-9
992E-68

992F-8D 0B 08

9935- C9 37

9937-90 2D

9937-90 2F FF

993C-20 FF FF

993F-20 FF FF

9942-20 FF FF

9947-A0 08

9949-A2 06

994B-20 FF FF
                                                                                       PUT FINAL SIGN ON X
                                                                                       CHECK FOR VERY SMALL
                                                                                      ...YES, SIN(X)=X*PI/2
PREPARE FOR POLYNOMIALS
X IN TEMP1
                                                    BCC
                                                    BCC .5
JSR MOVE.DAC.ARG
JSR MOVE.DAC.TEMP1
                                                     JSR DMULT
                                                                                                   XOX IN TEMP2
                                                    JSR MOVE.DAC.TEMP2
LDA #P.SIN
LDY /P.SIN
LDX #P.SIN.N
JSR POLY.N
           Ã2
20
20
                                                    JSR MOVE.DAC.TEMP3
```

## S-C Software Corporation

2331 Gus Thomasson, Suite 125, P.O. Box 280300, Dallas, Texas 75228 (214) 324-2050

#### S-C Macro Cross Assemblers

The high cost of dedicated microprocessor development systems has forced many technical people to look for alternate methods to develop programs for the various popular microprocessors. Combining the versatile Apple II with the S-C Macro Assembler provides a cost effective and powerful development system. Hobbyists and engineers alike will find the friendly combination the easiest and best way to extend their skills to other microprocessors.

The S-C Macro Cross Assemblers are all identical in operation to the S-C Macro Assembler; only the language assembled is different. They are sold as upgrade packages to the S-C Macro Assembler. The S-C Macro Assembler, complete with 100-page reference manual, costs \$80; once you have it, you may add as many Cross Assemblers as you wish at a nominal price. The following S-C Macro Cross Assembler versions are now available:

Motorola:	6800,1,2/6301 6805	\$32.50 \$32.50	RCA:	1802/1805	\$32.50
	6809 68000	\$32.50 \$50	Rockwell:	65C02	\$20
	66000	<b>\$</b> 50	DEC:	LSI-11	<b>\$</b> 50
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	8051	\$32.50		GI-1650	<b>\$</b> 50
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Zilog:	<b>z-8</b> 0	\$32.50			
	z-8	\$32.50			

The S-C Macro Assembler family is well known for its ease-of-use and powerful features. Thousands of users in over 30 countries and in every type of industry attest to its speed, dependability, and user-friendliness. There are 20 assembler directives to provide powerful macros, conditional assembly, and flexible data generation. INCLUDE and TARGET FILE capabilities allow source programs to be as large as your disk space. The integrated, co-resident source program editor provides global search and replace, move, and edit. The EDIT command has 15 sub-commands combined with global selection.

Each S-C Assembler diskette contains two complete ready-to-run assemblers: one is for execution in the mother-board RAM; the other executes in a 16K RAM Card. The HELLO program offers menu selection to load the version you desire. The disks may be copied using any standard Apple disk copy program, and copies of the assembler may be BSAVEd on your working disks.

S-C Software Corporation has frequently been commended for outstanding support: competent telephone help, a monthly (by subscription) newsletter, continuing enhancements, and excellent upgrade policies.

```
0951-
0953-
0955-
0957-
095A-
0950-
          A9
A0
                         LDA #Q.SIN
LDY /Q.SIN
               67
08
          A2
20
20
20
20
40
                02
                                              LDX
                                                     #Q.SIN.N
                                              JSR POLY.1
JSR MOVE.TEMP3.ARG
               FF
                    FF
               FF
FF
FF
                    FF
FF
                                              JSR DDIV
JSR MOVE.TEMP1.ARG
                                                                                     P/Q
                    FF
                                                                                     XP/Q
               FF
                    FF
                                              JMP DMULT
0963-
0966- A9
0968- A0
096A- 20
               9E
08
                                              LDA #CON.PI.2
LDY /CON.PI.2
JSR MOVE.YA.ARG.1
                                  .5
                                                                             FOR VERY SMALL X
SIN(2X/PI) = X*PI/2
                    FF
               FF
096D-
                                              JMP DMULT
                                  .
                                              TAN (DAC) = SIN(DAC) / COS(DAC)
0970-
0973-
0976-
0979-
0976-
0978-
0982-
0985-
                    FF
08
                                                                                SAVE ANGLE
               FF
                                              JSR PUSH.DAC.STACK
          20
20
20
20
20
20
20
40
                                  DP.TAN
                                                                                TAN=SIN/COS
               CE
                                              JSR DP.SIN
               FF
FF
                    FF
                                              JSR
                                                    POP.STACK.ARG
PUSH.DAC.STACK
                                                                                GET ANGLE
                                                                                SAVE SIN
                                              JSR
                    FF
08
                                              JSR
                                                     SWAP. ARG. DAC
                         2610
2610
2620
2630
2640
2650
                                              JSR DP.COS
JSR POP.STACK.ARG
JMP DDIV
                                                                                GET COSINE
GET SIN
SIN/COS
               BF
               FF
                    FF
0988-
                                 P. ATN
                                                     .EQ .
                                                                    HART # 5505
                                                                    P3#x^3 + P2#x^2 + P1#x + P0
03-
                          2660 P.ATN.N
                                                     .EQ 3
0988-
0988-
098E-
                    59
26
          453747354262
               1024116809398
102411922034
                    54
098E-
0991-
0993-
0996-
099E-
099E-
0944-
                         2670
                                                     .HS 42.12595.80226.30295.47240
                                                                                                             P3
                    55
64
06
                         2680
                                                     .HS 43.12557.91664.37980.65520
                                                                                                             P2
                    89
80
62
09A7-
09A9-
09AC-
09AF-
09B2-
                         2690
                                                     .HS 43.29892.80380.69396.22448
                                                                                                             P1
          43348
               19
09
54
                         2700
                                                     .HS 43.19720.30956.84935.02854
                                                                                                             PO
                         2710
2720
2730
09B4-
                                 Q. ATN
Q. ATN. N
                                                     .EQ 4
                                                                    X^4 + Q3X^3 + Q2X^2 + Q1X + Q0
09B4-
09B7-
09BA-
09BD-
                    06
          42
               37
86
19
01
20
60
61
          60
                    32
02
          27497746964082
                         2740
                                                     .HS 42.37066.08632.20190.23801
                                                                                                             Q3
                    76
17
46
09BF-
09C2-
09C5-
09C8-
                         2750
                                                    .HS 43.20769.26817.33604.63361
                                                                                                             Q2
09CA-
09CD-
               36
40
                    46
                    32
77
09D0-
09D3-
09D5-
09D8-
               7021993
                         2760
                                                    .HS 43.36466.24032.97707.76242
                                                                                                             01
                    72
56
09DB-
09DE-
                    50
                         2770
2780
                                                     .HS 43.19720.30956.84935.02861
                                                                                                             Q0
                         2790
2800
2810
2820
2830
2840
2850
2860
                                 ATN.TBL.H
                                                   /CON.PI.6
/CON.PI.2
09E0- 09
09E1- 08
                                             .DA
                                                    /CON.PI.3
                                              .DÃ
                                 ATN.TBL.L
                                                   #CON.PI.6
#CON.PI.2
#CON.PI.3
09E3-
09E4-
09E5-
         F1
9E
FC
                                             .DA
                                              .DA
                                              .DA
                         2870
09E6-
         40
49
31
06
                    79
24
27
               26
               19
12
47
57
29
08
09EC-
09EF-
09F1-
09F4-
                         2880 CON.TAN.PI.12 .HS 40.26794.91924.31122.70647
         40
98
98
73
                    35
55
88
                         2890 CON.PI.6
                                                    .HS 40.52359.87755.98298.87308
```

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```
09FC- 41 10 47
09FF- 19 75 51
0A02- 19 65 97
0A05- 74 62
0A07- 41 17 32
0A0A- 05 08 07
0A0D- 56 88 77
0A10- 29 35
                              2900 CON.PI.3 .HS 41.10471.97551.19659.77462
                              2910 CON.SQR.3 .HS 41.17320.50807.56887.72935
2920 ATN FUNCTION
                             ATN FUNCTION
1 OR 2 ARGUME
2950
2960 DP.ATN JSR AS.CHRGET
2970
2980 JSR AS.CHKOPN
JSR DP.EXP
                                                      1 OR 2 ARGUMENTS
0A12- 20 B1 00
0A15- 20 B8 DE
0A18- 20 FF FF
                                                      JSR AS.CHKOPN CHECK FOR (
                                                     JSR DP.EXP GET EXPRESSION JSR PUSH.DAC.STACK
0A18- 20
0A1B- 20
0A1E- 20
0A21- 20
0A24- C9
0A26- D0
0A28- 20
                              2990
3000
3010
3020
                  FF
                        FF
                 FF FF
                                                     JSR DP.TRUE
JSR AS.CHRGOT
                                                                              IN CASE 1 ARGUMENT
                 B7
2C
06
                        ÕÕ
                                                     CMP #1,
BNE .1
                                                                               TWO-ARG?
                              3030
3040
3050
3060
3070
                                                     BNE .1
JSR AS.CHRGET GOBBLE
JSR DP.EXP YES,GET OTHER ONE
JSR POP.STACK.ARG GET 1ST ARG BACK
190 AS.CHKCLS REQUIRE ")"
                                                                               NO
                 B1 00
FF FF
FF FF
BB DE
                       00
FF
0A2E- 20
0A31- 20
                                070
1080
                              3090
                                                                                   ARG/DAC
                                                     ATN (ARG, DAC)
                              3100
                                       .
                                110 DP.ATAN
0A34- AD 0B 08
0A37- OA
0A38- AD 17 08
0A3B- 6A
0A3C- 8D 19 08
                             3120
3130
3140
                                                                                        SAVE BOTH SIGNS
SIGN OF DENOMINATOR
SIGN OF NUMERATOR
                                                     LDA DAC.SIGN
                                                     ASL
                                                     LDA ARG.SIGN
                              3150
3160
3170
3180
3190
3210
                                                                                        BIT 7 = DENOM SIGN
BIT 6 = NUMER SIGN
                                                     ROR
                                              STA UV.SIGN
-CHECK FOR BOUNDARIES
0A3F- AD
0A42- FO
0A44- 38
0A45- AD
                 00 08
                                                     LDA DAC. EXPONENT
                                                                                        CHECK DENOMINATOR
                                                     BEQ .1
                                                                                         ...V/O, SO RETURN PI/2
                  0F
                 OC 08
                                                     LDA ARG. EXPONENT
                             3230
3230
3240
3250
3250
32280
32280
                                                     BEQ .12
SBC DAC.EXPONENT
0A48- FO
                 17
00 08
                                                                                         ...0/U, SO RETURN 0
OA4A- ED
                                                    CMP #20
BCC .11
0A4D- 30
0A4F- C9
0A51- 90
                 0E
                                                                                        IF >10^20, RETURN PI/2
...NOT >10^20
V/O OR OVERFLOW
SO RETURN PI/2
0A51 - 90
0A53 - A9
0A55 - A0
0A57 - 20
                  14
                                                     BCC .11
LDA #CON.PI.2
LDY /CON.PI.2
JSR MOVE.YA.DAC.1
                  9E
                                      . 1
            ÃÓ Ó8
20 FF FF
0A5A- 4C
0A5D- C9
0A5F- B0
0A61- 20
                 2A OB
C1
06
                                                     JMP DP.ATN.C
CMP #-63
BCS .11
                             IF <10^-63, RETURN 0
                                       .13
                                      .12
                                                     JSR DP.FALSE
                 FF
                      FF
                                                                                        RETURN O
                                      :14
0A64- 4C
                 1B 0B
                                                     JMP DP.ATN.B
JSR DDIV
                FF
00
36
F3
           20
AD
C9
90
0A67-
0A6A-
                      FF
08
                                                                                        CALCULATE V/U
                                      LDA DAC. EXPONENT
CMP #$40-10
BCC .14
---FOLD AT PI/4-----
                                                                                        IF X VERY SMALL, ATAN ... VERY SMALL INDEED!
                                                                                                                        ATAN(X)=X
0A6D-
OA6F-
0A71- A9
0A73- 8D
0A76- 8D
                                                                                        GET ABS(X), BECAUSE
SIGNS ALREADY REMEMBERED
                                                     LDA #0
                 00
                      08
                 0B
                                                     STA DAC.SIGN
                 18 08
00 08
                                                     STA N
                                                    LDA DAC. EXPONENT CMP #$41 BCC .3
0A79- AD
                                                                                        IS X<1?
0A7C- C9
0A7E- 90
0A80- A9
                 ÓĖ
                                                                                           .YES.
                                                                                                      X<1
                                                    LDA #CON.ONE
LDY /CON.ONE
JSR MOVE.YA.ARG.1
                 7D
08
                                                                                        FORM RECIPROCAL
0A82- A0 08
0A84- 20 FF FF
0A87- 20 FF FF
                                                     JSR DDIV
                                                                                        1/X
                7490
02 35510
18 08 35520
FF FF 355340
09 35550
FF FF 35570
0B 08 35580
FF FF 3560
FF FF 3560
FF FF 3560
FF FF 3560
FF FF 3600
FF FF 3600
0A8A-
0A8C-
          A9
8D
                                                     LDA #2
                                                                                        AND REMEMBER WE DID IT
                                            0A8F- 20
                                      • 3
                                                    LDA #CON.TAN.PI.12 TAN(PI/12)
LDY /CON.TAN.PI.12
JSR MOVE.YA.ARG.1
0A92- A9
0A94- A0
0A96- 20
0A99- 20
                                                     JSR DSUB
                                                                                         IS X>TAN(PI/12)?
0A9C-
0A9F-
          AD
48
                                                    LDA DAC.SIGN
                                                    PHA
0AA0- 20 FF
0AA3- 68
0AA4- 10 2F
                             3600
3610
                 FF FF
                                                     JSR MOVE. TEMP1. DAC RESTORE X
                                                    PLA
                             3620
                                                    BPL .4
                                                                                          ...NO. WE DON'T HAVE TO FOLD
```

```
OAA6- EE 18 08 3630
OAA9- A9 07 3640
OAAB- A0 0A 3650
OAAB- 20 FF FF 3660
OAB0- 20 FF FF 3680
OAB6- 20 FF FF 3680
OAB6- 20 FF FF 3710
OAB6- 20 FF FF 3720
OAB6- 20 FF FF 3720
OAB6- 20 FF FF 3750
AC2- A0 0A 37340
AC6- 20 FF FF 3750
AC9- 20 FF FF 3760
AC9- 20 FF FF 3770
ACF- 20 FF FF 3780
AC9- 20 FF FF 38800
AC9- 20 FF FF 38800
DB- A9 88 83840
E2- A0 09 38860
E2- A2 07 FF FF 38870
E2- A2 07 FF FF 38870
E2- A2 07 FF FF 38890
DB- A9 88 E0- A0 09 38900
E2- A2 07 FF FF 38890
C2- A0 09 39900
C3- A0 08 40 10 - A0 08 08 40 20 - A0 0
                                                                                                                                                INC N
                                                                                                                                                                                                                                                   ...YES, SO FORM
(X*SQR(3)-1) / (SQR(3)+X)
                                                                                                                                               LDA #CON.SQR.3
LDY /CON.SQR.3
                                                                                                                                                JSR MOVE.YA.ARG.1
                                                                                                                                               JSR DMULT
                                                                                                                                                                                                                                                  X*SQR(3)
                                                                                                                                                JSR MOVE.DAC.ARG
                                                                                                                                            JSH DSUB

JSR MOVE.DAC.TEMP2 SAVE IT
JSR MOVE.TEMP1.ARG GET X
LDA #CON.SQR.3
LDY /CON.SQR.3
LDY /CON.SQR.3
JSR MOVE.YA.DAC.1
JSR DADD
                                                                                                                                               JSR DP. TRUE
                                                                                                                           JSR MOVE.DAC.TEMP1 SAVE FOLDED-UP X
-ATAN(0...PI/12)---------
JSR MOVE.DAC.ARG
JSR DMILT
                                                                                                                                               JSR MOVE.TEMP2.ARG
                                                                                                                                               JSR DMULT
                                                                                                                                               JSR MOVE DAC . TEMP2 SAVE X^2
                                                                                                                                             JSR MOVE JAC. TEMP2 SAVE :
LDA #P.ATN
LDY /P.ATN
LDX #P.ATN.N
JSR POLY.N
JSR MOVE DAC. TEMP3
LDA #Q.ATN
LDX #Q.ATN
LDX #Q.ATN.N
JSR POLY.1
JSR MOVE. TEMP3.ARG GET P
JSR MOVE TEMP3.ARG GET P
JSR MOVE TEMP3.ARG P/O
                                                                                                                                              JSR DDIV P/Q
JSR MOVE.TEMP1.ARG GET X
JSR DMULT P(X^2)/Q(X^2)*X
                                                                                                                         -UNFOLD FROM PI/12, PI/4---
                                                                                                                                                                                                                                            0, 1, 2, OR
...NO ADDEND
                                                                                                                                                                                                                                                                                        OR 3
                                                                                                                                               BEQ DP.ATN.B
                                                                                                                                            BEQ DP.ATN.B
DEX
DEX
DEX
.5
LDA DAC.SIGN
EOR #$80
STA DAC.SIGN
LDA ATN.TBL.L,X
LDY ATN.TBL.H,X
JSR MOVE.YA.ARG.1
JSR DADD
SOLD INTO CHARDRANTS
                                                                                                                                                                                                                                            0, 1, OR 2
...NO COMPLEMENT
  0B07- AD 0B 08 4020
0B0A- 49 80 4030
0B0C- 8D 0B 08 4040
                                                                                                                                                                                                                                            ATAN(1/X)=ATAN(PI/2 - X)
 0B0F- BD E3 09
0B12- BC E0 09
0B15- 20 FF FF
0B18- 20 FF FF
                                                                                4050 .5
                                                                                                                                                                                                                                            GET A(N)
                                                                                4060
                                                                               4070
4080
                                                                                                                                                                                                                                            X + A(N)
                                                                                 4090 ---- UNFOLD INTO QUADRANTS-----
                                                                                 4100 DP.ATN.B
0B1B- 2C 19 08 4110

0B1E- 10 0A 4120

0B20- A9 A9 4130

0B22- A0 08 4140

0B24- 20 FF FF 4150

0B27- 20 FF FF 4160
                                                                                                                                            BIT UV.SIGN
BPL DP.ATN.C
LDA #CON.PI
LDY /CON.PI
                                                                                                                                                                                                                                          TEST SIGN OF DENOMINATOR
...POSITIVE, 1ST OR 4TH
...NEGATIVE, 2ND OR 3RD
SO DO PI-X
                                                                                                                                             JSR MOVE.YA.ARG.1
JSR DSUB
                                                                               4170 4-180 DP.ATN.C
                                                                                                                                           BIT UV.SIGN
BVC .6
LDA DAC.SIGN
EOR #$80
STA DAC.SIGN
 0B2A- 2C 19 08 4190
0B2D- 50 08 4200
                                                                                                                                                                                                                                          TEST SIGN OF NUMERATOR
...POSITIVE, 1ST OR 2ND
...NEGATIVE, 3RD OR 4TH
0B2F- AD 0B 08
0B32- 49 80
0B34- 8D 0B 08
0B37- 60
                                                                           4210
4220
4230
4240
                                                                                                         .6
                                                                                4250
```

#### EPROM Programmer

A new EPROM Programmer, called the PROMGRAMER, is out from SCRG (the makers of quikLoader). This one burns anything from 2716's up to 27256's, and retails at \$149.50. We'll sell 'em to you for a nice round \$140. The software comes on disk, with instructions for loading it into EPROM for the quikLoader card.

...also burns 27512's!

More Detail on Using 65C02's in old Apples.....Andrew Jackson

In recent issues of AAL there have been several articles on the 65C02 and how to get it running in the Apple II+. I too was keen to get a 65C02 working in my machine, and had spent some time trying to get first a lMHz part and then a 2MHz part to work.

William D. O'Ryan's letter in the June 84 AAL prompted me to try again and I am happy to report that the modification he described does work (replacing the LS257's at B6 and B7 with F257's). I wanted to find exactly why I could not simply substitute a 65C02 for a 6502, and so I spent some time looking at the circuit and specifications, using an oscilloscope to check my results.

The reasons that I eventually came up with are as follows. The Apple II circuit relies on various 'features' of the 6502 so that all the various parts of the Apple will work. The circuit diagram shows that the system timing is derived from  $\emptyset$ 0; the 6502 actually expects system timing to be derived from  $\emptyset$ 2. There is a slight delay between these two signals: on a 6502 it is about 50ns and on a 65C02 it is about 30ns. This difference in delays is what causes the problems when fitting a 65C02.

To simplify its circuit design the Apple uses a rather dirty trick when reading data from RAM memory. Normally when the 6502 reads data it expects the data on the bus to be valid 100ns before the end of  $\emptyset 2$ , and it latches the data into its internal registers when  $\emptyset 2$  changes. The setup time allows the data bus to settle into a consistent state before being read. The Apple reduces the setup time to about 45 ns (worst case). This setup time would be ample for the 65C02 were it not for the shift between  $\emptyset 0$  and  $\emptyset 2$ ; this shift reduces the setup time to 25ns. A 2MHz 65C02 specifies a MINIMUM 40ns setup time; obviously there is a -15ns tolerance on the setup time, and hence the processor works erratically when timings fall into worst case conditions.

The tolerance is regained by substituting 74F257's for the two 74LS257's at board locations B6 and B7. These two chips multiplex the RAM data and the keyboard data; in doing so they add a delay of 30ns worst case to the data. By substituting F257's, the added delay is reduced to 5 ns; this changes the tolerance on the data setup time from -15ns to +10ns.

The Apple //e must use a slightly modified technique when reading data from RAM which explains why a 65C02 works in it without any modifications. I cannot check this as I do not have a //e circuit description. Anyway, it is probably all inside the MMU chip.

[ The 65816 specifications state a minimum read data setup time of 50ns, 10ns longer than the 65C02. One AAL reader has called us to report that the 65802 works wonderfully well in his old II+, even better than the original 6502. Some of you have wondered where to get the F257's: try Jameco Electronics, 1355 Shoreway Road, Belmont, CA 94002, phone (415) 592-8097. Their ad in Byte, Dec '84, page 349, says they have 74F257's at \$1.79 each. (editor)

Gary Little's New Book, "Inside the Apple //e"

This is a useful book. The kind you want to keep, read, and constantly use as a reference. About 400 pages thick, 6x9, published by Brady Communications at \$19.95.

Gary, a lawyer in Vancouver, has been serious about Apples since 1978 (almost as long as me). He's a long-time subscriber to AAL, Call APPLE, and other sources of the in-depth knowledge crammed into his book. He's also a programmer, with serious software on the market such as "Modem Magician". He knows what he's writing about, and writes it well.

A walk through the chapters may be the quickest way to get the measure of the book.

- 1--condensed history of Apple; intro. to binary, hex, and assembly language.
- 2--inside the 6502 itself: zero page, stack, registers, status, opcodes, address modes, I/O, interrupts, and the memory layout in the //e.
- 3--the Apple monitor: the commands explained, plus a table of the most useful subroutines in the monitor ROM.
- 4--Applesoft: memory map, tokenization, variable storage, integer and real numbers, the CHRGET subroutine, linking to assembly language programs, subroutines in ROM, and more.
- 5--DOS: internal structure, memory map, page 3 vectors, VTOC, catalog, track/sector lists, RWTS, and a read.sector program. ProDOS: memory map, page 3 vectors, volume bit map, directory, MLI, and a read.block program.
- 6--character input and the keyboard: RDKEY, 80-column firmware, RDCHAR, reading a line, changing input devices, encoding of keys, auto-repeat, type-ahead, all about RESET.
- 7--character and graphic output: too much to list here, all the way through double hi-res.
- 8--memory management: bank switching of ROM and RAM, auxiliary RAM, running co-resident programs.
- 9--speaker and cassette ports: music and voice.
- 11--peripheral slots: I/O memory locations, slot ROM, expansion
  ROM, scratchpad RAM, auxiliary slot, software protocols.

Many useful and interesting programs are listed in the book. There is an optional diskette available (coupon bound in the book offers it for \$20). The diskette also includes a few bonus utility programs for use with DOS 3.3, including RAMDISK and DISK MAP.



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#### S-C Assembler (Ver 4.0 only) SUPPORT UTILITY PACKAGE (\$30.00)

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- \* SC.GSR Global Search & Replace eliminates teadious manual renaming of tabels. Search all/part of source.
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\*\*\*\*\*\*\*\*\*\*

Each chapter ends with a bibliography of related books, manuals, and articles. (You'll find lots of references to AAL.)

If you grew along with Apple, as I did, you probably don't really need this book. On the other hand, you will still enjoy it, and probably want it for you collection. If you are relatively new, and having difficulty gathering all the information from past publications and scattered sources, you will want Gary's book too.

As you might suspect, we like the book so well we have decided to stock it. You can get from us for \$18 plus shipping (and tax where applicable).

Correction re MVN and MVP in 65802.....Bob Sander-Cederlof

In the October AAL I presented a general memory mover written in 65802 code. I stated that the MVP and MVN instructions took 3 cycles-per-byte during the move. I was wrong.

In looking through small tiny print in the preliminary documentation for the chip, I came across the number "7". Shocked, I wrote a little test program which moved 10000 bytes 1000 times. That means the MVN in my test would move a total of 10,000,000 bytes. With a stop watch I clocked the running time at just under 70 seconds. If it had been 3 cycles-per-byte, the test would have run in 30 seconds.

I don't know how I got that "3" in my head, but the right number is "7". Still considerably faster than 6502, though.

1000	*SAVE S.TIME MVN 1010 .OP 65816 1020 .OR \$300
00-	1030
000300- 18 000301- FB 000302- C2 30	1050 #
000304- A9 E8 03 000307- 85 00	1110 LDA ##1000 1120 STA CNTR
000309- A2 00 30 00030C- A0 00 40 00030F- A9 0F 27 000312- 54 00 00 000315- C6 00	1130 *** 1140 .1 LDX ##\$3000 Source start address 1150 LDY ##\$4000 Destination start address 1160 LDA ##9999 # Bytes - 1 1170 MVN 0,0 1180 DEC CNTR 1190 BNE .1
000317- 38 000318- FB 000319- 60	1200 *

Strange Way to Divide by 7......Bob Sander-Cederlof

Division by seven is a necessary step for hi-res plotting routines. The quotient is the byte index on a given scan line. The remainder gives the bit position within that byte.

The hi-res code inside the Applesoft ROMs uses a subtraction loop to divide by seven, which can loop up to 36 times at 7 cycles per loop. This is a maximum of over 250 cycles, which is why super-fast hi-res usually uses lookup tables for the quotient and remainder.

I stumbled on a faster way of dividing any value up to 255 by seven. This is not directly usable by standard hi-res, because the x-coordinate can be as large as 279. My trick also does not give the remainder, just the quotient.

Here is the program, along with a test routine which tries every value from 0 to \$FF, printing the quotient. The output from the test program is also shown, and you can see that the quotient is correct in every case. Can you explain why it works?

[ Hint: 1/7 = 1/8 + 1/64 + 1/512 + 1/4096 + ... ]

	1000 *SAVE S.FUNNY DIVIDE BY SEVEN
00-	1020 BYTE .EQ 0
0800- A9 00 0802- 85 00 0804- A2 07 0806- E0 07 0808- D0 05 0808- 20 ED FD 0806- 20 ED FD 0805- 20 DA FD 0817- F0 09 0817- F0 09 0818- D0 EA 0816- 20 8E FD 0818- 04 08	1040 T
0823 - A5 00 0825 - 4A 0826 - 4A 0828 - 65 00 0828 - 6A 0828 - 4A 082C - 4A 082D - 65 00 0830 - 4A 0831 - 4A 0831 - 4A	1200

It is possible to divide by 3 or 15 using a program based on the same principle as the divide-by-seven above. Here is the code for those.

	DIVIDE.BY.FIFTEEN	1210	DIVIDE.BY.THREE
1220	LDA BYTE	1220	LDA BYTE
1230 1240 1250 1260	LSR	1230 1240	LSR
1240	LSR	1240	LSR
1250	LSR	1250	ADC BYTE
1200	LSR	1260	ROR
1270	ADC BYTE	1270	LSR
1270 1280 1290	ROR LSR	1280	ADC BYTE
1290		1290	ROR
1300	LSR	1300 1310	LSR
1310	LSR		ADC BYTE
1320 1330 1340	ADC BYTE	1320	ROR
1320	ROR	1330 1340	LSR
1340	LSR	1340	ADC BYTE
1350	LSR	1350	ROR
1350 1360 1370	LSR	1350 1360 1370	LSR
13/0	RTS	1370	RTS

Using the divide by 15, you could make a divide by ten. First multiply the original number by three (by shifting one bit left and adding), then divide by 15 using the above program, and then by 2 (by shifting one bit right). Since 3X/30 = X/10, there you have it.

BLANKENSHIP BASIC	1000	REM sample listing
For the Apple II+, IIe, and IIc	1010	
101 the apple 11.7, 110, the 100	1011	• • • • • • • • • • • • • • • • • • • •
1. WHILE-ENDWHILE and REPBAT-UNTIL loops	1015	DIN(N)
2. True IF-THEN-ELSE-ENDIF (Using WHEN)	1020	., -
3. PRINT.USING, FILE, MERGE, RANDOMIZE	1030	
4. PRINT and TAB commands work in HIRES	1040	
5. 80 columns supported on IIe and IIc	1050	, , , , ,
6. Full Editor with AUTO-NUM and RENUM	1060	
7. Listings are indented automatically	1070	PRINT N,
8. Fast SORT, SEARCH and INSTR\$ commands	1080	
9. BOX, BOXFILL, DRAW.USING and SOUND	1090	ENDWHEN
10. No more CHR\$(4) for DOS commands	1100	PRINT SIN (N)
11. DEFINE and PERFORM named proceedures	1101	
12. 99% Upward compatible with Applesoft	1110	UNTIL N = 20
	1120	END
** Introductory Offer \$20 postpaid **		
	1130	DEFINE "OUTPUT"
Noney back if not entirely satisfied!	1140	
	1150	RBM procedure
mail check to: Only	1160	FINISH
mail check to: John Blankenship P.O. BOX 47934 Atlanta GA 30362		
P.O. BOX 47934 05 3		DEFINE "INPUT"
Atlanta GA 30362	1180	
	1190	FINISH

Sly Hex Conversion......Bob Sander-Cederlof

Have you ever wondered what would happen if you added, in the 6502 decimal mode, values that were not decimal? I have. I also wondered if any of the results might be useful.

For example, what happens if I add 0 to \$0A, in decimal mode? The following little piece of code will tell me:

CLC

SED set decimal mode

LDA #\$0A ADC #0

ADC TO

CLD clear decimal mode

JMP \$FDDA monitor print byte routine

Lo! The \$0A turns into \$10! It makes sense, because of course adding zero does not change anything. But the automatic "decimal adjust" that occurs after the add when the 6502 is in decimal mode detects the "A" nybble, generates a carry to the next nybble, and subtracts \$0A.

It turns out the same process turns \$0B into \$11, \$0C into \$12, and so on up to \$0F into \$15.

That is a useful result! That means that I can convert a hex nybble to BCD byte by merely adding zero when in decimal mode!

A little further experimentation will lead to another useful trick. If I add first \$90 and then \$40, both additions in decimal mode, a value between \$00 and \$0F will be converted to the ASCII code for the digits 0-9 and letter A-F. Believe it or not!

The first addition, of \$90, gives us \$90-\$9F. The automatic "decimal adjust" does nothing to \$90-\$99, and carry will be clear afterwards. If the intermediate result was \$9A-\$9F, the decimal adjust will first generate a nybble carry because the A-F nybble is greater than 9, and reduce that nybble by A. The nybble carry will increment the 9 nybble to A, which gets reduced back to 0 and a byte carry is set. This means we end up with \$90-\$99 with carry clear or \$00-\$05 with carry set.

Adding \$40 in the next step brings the \$90-\$99 up to \$30-\$39 (with carry out of the byte, which we will ignore). The \$00-\$05 will be brought up to \$41-\$45, ASCII codes for A-F. Voila!

Useful, but maybe not the best. It turns out that a more traditional approach is only one byte longer and saves a few cycles. With the value \$00-\$0F in the A-register:

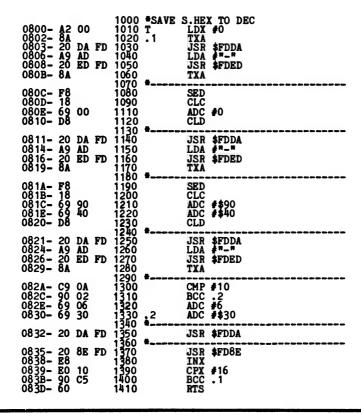
CMP #\$0A

BCC .1 0-9

ADC #6 convert A-F to \$11-16

.1 ADC #\$30

will convert to ASCII.



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There is a lot of grumbling going on, or at least so says the media. Supposedly Mac owners are MAD over Apple's \$995 price tag for the 512K upgrade kit. And the fact that new buyers get a lower system price makes them even madder.

If it's true, then I guess the computer "for the rest of us" has found a market with a real-estate or Detroit mentality. Haven't they noticed that prices on virtually all electronic items go down every year? (I always say, "If houses and cars had gone the way electronics has over the last 30 years, we would now be able to buy a 3-bedroom home for two dollars and a nice car for 50 cents. Of course they would both fit on the head of a pin...")

I remember when I bought my Apple, with two rows of 4K RAM chips totalling 8K bytes. Adding another row of 4K chips would have cost me about \$50. The price at that time for one set of 8 16K chips was \$520. Through a special arrangement at Mostek, members of our local club were able to get them for \$150. So to raise my Apple from 8K to 48K cost me \$450. Retail price would have been \$1560, plus tax.

Looking back even further, I found a letter from a Raymond Hoobler to the editor of the Journal of Dentistry, from October 1976. Ray owned an Apple 1, which was populated with 1K RAM chips. He was VERY happy with Apple's promise of an upgrade kit consisting of 4K RAM chips for ONLY \$500!

It will not be too long before the price of 256K RAMs drops. Then we can start grumbling about the price of 4-megabyte upgrade kits. Or, we could rejoice at the blessings of ever improving technology, mass marketing, and understanding wives.

#### APPLE SOFTWARE ENGINEER

Applied Engineering, a major manufacturer of Apple peripherals, has an opening for a 6502 Machine Language Programmer. Apple experience is required.

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Generating Tables for Faster Hi-Res.....Bob Sander-Cederlof

Look on page A23 in the Apple Supplement in the back of the December 1984 issue of Byte for an excellent article for the hi-res graphics buff: "Preshift-Table Graphics on Your Apple", by Bill Budge, Gregg Williams, and Rob Moore.

The article presents another of Bill Budge's secrets for fast animation using block graphics. If you want to move a block a few dots left or right, it is time-consuming to shift the 7-bits-in-8 dot images. Older techniques stored pre-shifted sets for each image that might be moved. The neater method described in this article stores a 14x256 byte table of all possible shifts of all possible bytes, and uses a fast lookup technique. I am not going to repeat all that here ... get the article.

The article also included some sample programs that used two other tables: a 192 entry address table for the addresses of each hi-res line, and a 280 entry table for the quotient and remainder of each horizontal position. Both of these tables were originally generated by Applesoft programs, and BSAVEd. The example program BLOADed them.

It dawned on me that a machine language program to generate those two tables would take less than half a page of code and be considerably faster than BLOADing pre-generated tables. Furthermore, once the tables were generated, the half-page of code could be overlaid with other programs or data. In a commercial product, this could cut down the boot time significantly.

First I wrote a program to generate the 192 addresses. This was almost a hand-compilation of the Applesoft program in the Byte article, but not quite. (I wrote the comments in near-Basic, as you can see.)

Then I merged into that program the stuff to generate the first 192 quotients and remainders. This is the horizontal dot position divided by 7 (7 dots per byte) to give the byte position on the line and the bit position in that byte.

After the 192 trips through that code, I added a loop to generate the rest of the Q/R pairs, from dot position 192 up to 279.

I timed the program by running it 250 times. All 250 took roughly 3 seconds, which means building the tables once takes about 12 milliseconds. Compare that to loading them from disk, which would take at least a half second.

I haven't tried it yet, but I think the preshift tables which were the meat of the Byte article could also be generated by a machine language program much quicker than BLOADing the same. And since the program only needs to be used once, during initialization, it too could be burned after using.

```
1000 *SAVE S.MAKE HIRES ADDRS
                                   1010
                                  1020 I
1030 JL
1040 JH
00-
                                                              .EQ
                                                             EQ
01-
                                                                      1
 02-
                                                                      234
                                  1050
1060
1070
1080
 Ŏ3-
                                                             .EQ
 ŏ4-
 05-
                                                                      5
                                                             .EQ $900
.EQ $900
.EQ $A80
.EQ QUO.1+192
.EQ QUO.1+280
.EQ REM.1+192
                                   1090
1100
 0900-
                                             ADDRL
0960-
0A80-
                                             ADDRH
                                  1110
1120
1130
1140
                                             QUO. 1
0B40-
0B98-
0C58-
                                            QUO.2
REM.1
                                             REM. 2
                                  1150 #----
1160 BUILD
0800- A2
0802- 86
                                                            LDX #
                                                                                          FOR X = 0 TO 191 STEP 1
FOR I = 0 TO $50 STEP $28
FOR J = 0 TO $0380 STEP $0080
                    00
                                                                      #0
                    ŏŏ
                                  1 170
1 180
1 190
             86
86
86
86
0804 -
0806 -
                                                             STX JL
STX JH
STX K
STX Q
                    01
                    ÕŻ
0808-
080A-
080C-
                    03
                                                                                          FOR K = 0 TO QUOTIENT = 0
                                                                                                                       $1C STEP $04
                                   1200
                                  1210
1220
1230
1240
1250
1260
                                                             STX R
                                                                                          REMAINDER = 0
                    05
                                                      BUILD NEXT HI-RES ADDR-
080E- A5
0810- 05
                    00
                                             . 1
                                                             LDA I
ORA JL
                   ŎÌ
                                                             STA ADDRL,X
LDA #$20
 0812-
             9D
                    00 09
0815- A9
0817- 05
0819- 05
                                  1270
1280
1290
                   20
02
03
                                                             ORA JH
ORA K
                                                     STA ADDRH, X
-SAVE NEXT Q/R PAIR
LDA Q
STA QUO.1,X
                    CŌ
                           09
                                  1300
1310
1320
1330
1340
1350
1370
1380
1390
1400
081E- A5
0820- 9D
0823- A5
0825- 9D
                   04
80
                          OA
                   05
98 0B
                                                             LDA R
                                                             STA REM. 1,X
                                                     NE XT K
0828- 18
0829- A5
082B- 69
082D- 85
082F- 49
                   03
04
03
20
                                                            LDA K
ADC #4
STA K
EOR #$20
                                  1420
1430
1440
1450
1460
0831- DO
                                                             BNE
                                                                    . 2
                    1B
                                                   --NEXT J
0833- 85
0835- A5
0837- 49
0839- 85
083B- D0
083B- E6
083F- A5
0841- 49
0843- D0
                                                            STA K
LDA JL
EOR #$80
                   03
01
80
                                  01
                                                             STA JL
                                                             BNE .2
INC JH
                   11
02
                    02
                                                             LDA JH
                    04
                                                             EOR #4
                                                             BNE
                                                      NEXT I
0845-
0847-
0848-
0848-
0846-
            85
18
                    02
                                                             STA JH
CLC
                                                            LDA I
ADC #$28
             45
69
85
                    00
28
                                                      STA I
BUMP Q/R PAIR
084E- E6
0850- A5
0852- 49
0854- D0
0856- 85
0858- E6
                                                            INC R
                                                                                          R COUNTS 0...6
                    05
05
                                             .2
                                                                                         IF R=7, MAKE 0 AND BUMP Q ...NOT 7 YET ...R=7, SO MAKE IT 0 AND BUMP Q
                   07
                                                            EOR #7
BNE .3
                                  1630
1640
1650
                    05
                                                            STA R
INC Q
                                  1660
1670
1680
1690
                                                     NE XT X-
085A- E8
085B- E0 C0
085D- 90 AF
                                             • 3
                                                             CPX #192
                                                            BČČ
```

```
1700 *---NOW FINISH Q/R PAIRS-----
1710 *---BETWEEN 192 AND 279-----
1720 LDX #0 FOR X = 0 TO 280-192-1
1730 .4 LDA Q
1740 STA QUO.2,X
1750 LDA R
1750 LDA R
085F- A2 00
0861- A5 04
0863- 9D 40 0B
0866- A5 05
0868- 9D 58 0C
                                                    STA REM. 2.X
*---BUMP Q/R PAIR AS BEFORE--
INC R
                                        1760
                                        1770
1780
086B- E6 05
086D- A5 05
086F- 49 07
0871- D0 04
                                        1790
1800
1810
                                                                       LDA R
                                                                       EOR #7
                                                                       BNE
0873- 85
0875- E6
                                        1820
                                                                       STA R
                                        1830
1840
1850
                                                              ĬŃĊ Q
-NEXT<u>X</u>---
0877- E8
0878- E0
087A- 90
087C- 60
                                                                       ÎNÂ
                                                                       ĈPX #280-192
BCC .4
                                        1860
1870
1880
                                                                       RTS
                                        1890
```

Blanken ip's Basic......Bob Sander-Cederlof

John Blankenship has put together an Applesoft enhancement package, at a mouth-watering price. (See his ad elsewhere in this issue for his \$20 introductory offer.) He sent me a review copy, so I tried it out.

BBASIC is a large chunk of machine language code that sits between HIMEM and the DOS file buffers. It also sits between you and Applesoft, hiding itself behind a facade of new editing and listing features. BBASIC takes control even in direct mode, giving you an EDIT command, structured listings, and the ability to skip out of long catalogs.

In pure BBASIC, line numbers are used only as line numbers, not as destinations for GOTOs or GOSUBs. A built-in RENUM command soon convinces you to live this way and like it. In place of line-number branches, you use alphabetic "names" for subroutines, and WHEN-ELSE-ENDWHEN for logic flow. John has also added WHILE-ENDWHILE, REPEAT-UNTIL, CASE, and other structured looping and branching words.

During execution, a special COMPILE verb creates a table of "names" used in your program. This speeds up execution.

Hires Text generation is built-in, along with some extensions to the hires graphics. Musical tone generation with control over pitch, duration, and timbre is also included. You also get SORT, SEARCH, and PRINT USING.

I am just scratching the surface. I didn't like every feature, but there is plenty left over. Worth a lot more than \$20.

By the way, if John's name sounds familiar, it may be because he is the author of "The Apple House", a book on controlling your home published by Prentice-Hall. John also is a Professor at DeVry Institute. A Solution to Overlapping DOS Patches............Paul Lewis Fairfax, Virginia

I have recently resolved a compatibility problem between two desirable sets of DOS 3.3 patches: the RAMdisk of the 192K Neptune extended memory card, and the DOS Dater that comes with Applied Engineering's Timemaster II. It seems they both want to put patches into the same "unused" spaces inside DOS.

After examining the two patches carefully, I found out which parts of the patches were overlapping. Being unable to find a truly unused area inside DOS, I used the technique on page 7.3 of "Beneath Apple DOS" of placing routines in the "safe" area between DOS and its buffers. This seems to work fine. [Until you try to run some other program that does the same thing, like PLE... (editor) ]

The file DATER.OBJ0 contains the DOS.DATER patch that I use. I noticed that the patch could be placed anywhere, since there are no internal references. Using an Applesoft program (part of my HELLO), I move the DOS buffers down far enough to fit this code in, and then BLOAD the patches.

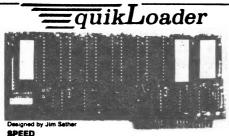
- 100 PRINT CHR\$(4) "BRUN AUTO NEPTUNE"
- 110 PRINT "PSEUDO DISK INSTALLED"
- 120 POKE 40192,128 : REM Lower the buffers
- 130 PRINT CHR\$(4) "MAXFILES 3"
- 140 PRINT "BUFFERS MOVED"
- 150 PRINT CHR\$(4) "BLOAD DATER.OBJO, A\$9CDO"
- 160 POKE 45571,15 : REM Patch file name length
- 170 POKE 42883,14
- 180 POKE 44085,208 : REM Hook DOS to the DATER code
- 190 PORE 44086,156
- 200 PRINT "DOS DATER INSTALLED"

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The quikLoader is the fastest way to load programs, BAR NONEI Applesoft, Integer, or machine language programs can be loaded in fractions of a second. More importantly, DOS is instantly loaded every time the computer is turned on. Integer is even loaded in the language card. This process takes less than a second, saving valuable time. The quikLoader operating system can keep track of over 250 programs stored in PROMs (Programmable Read Only Memory). The user simply transfers any of these programs to PROM using the instructions packed with the unit, and any PROM programmer, or we will provide this service.

#### CONVENIENCE

How many times have you started to work with a frequently used program, only to find that you have misplaced the disk, or worse, had the disk damaged, or the dreaded "I/O ERROR" message flash on the screen. With the quikLoader, these nightmares can be a thing of the past. Frequently used programs are available instantly when you need them, without having to look for the disk, or hoping that the lengthy disk loading procedure goes smoothly. If you do need to use standard disks, the quikLoader even speeds up that process. For example, to catalog a disk, just press ctrl-C Reset, To run the "HELLO" program, press ctrl-H Reset. Other "onekey" commands include entering the monitor, booting the disk, calling up the mini-assembler, etc. The major difference between the quikLoader and the other ROM cards is the complete operating system (in PROM). This enables you to get the quikLoader catalog on the screen (by pressing ctrl-Q Reset), allowing you to see what programs are available. Loading or running of the desired program requires one keypress. Program parameters, such as starting address and length of machine language programs can be seen on the catalog screen, if desired. **EASE OF USE** 

The quikLoader plugs into any slot of the APPLE][or//e. The card is reset driven. To use any of the many features of the card, RESET is pressed in conjunction with a key. The particular key pressed chooses the feature.

VERSATILE
The quikLoader will accept any of the popular PROMS available on the market, 2716, 2732, 2764, 27128 and 27256. These types may be freely intermixed on the card. Long programs can take up more than one PROM, or several short programs may be stored on one PROM. The quikLoader operating system even handles multiple cards, so you can easily double or triple the amount of PROM memory available. The ultimate memory capacity

of one card is 256K, so many frequently used programs and utilities can be stored. We even start your library of programs with the most popular utilities on the card, FID and COPYA. Now, if you have to copy a disk, you don't have to search for the master disk. You can start copying within 3 seconds after turning on the computer.

INCREASED DISK CAPACITY

Since DOS is loaded from the quikLoader every time the computer is turned on, it is not necessary to take up valuable disk space with DOS. This will give you more than 5% additional space for programs and data on your disks.

#### SYSTEM REQUIREMENTS

The quikLoader will work in an APPLE ] [, ] [+, or //e. If used in a ] [+, a slightly modified 16K memory card is required in slot 0. A disk drive is required to save data.

#### \$179.50

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These items are also available from S-C Software.

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